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EE 121 is an introduction to digital circuits and their applications. Topics covered in lecture are explored in weekly laboratory assignments and a two-week final project. Lecture topics include: measurement techniques logic

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families switching speed Boolean
algebra state machines computer-aided
design (CAD) logic simulation digital
data ...

EE 121: Digital Design Laboratory - Stanford University

program that describes the behavior of
the digital circuit. The program can then

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be used to both simulate the operation of the circuit and synthesize an actual implementation of the circuit in a CPLD, an FPGA, or an application specific integrated circuit (ASIC). Another recent trend is to design digital circuits using block diagrams or graphic

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